

MIMAS

EVT BUILD

2010-03-18

DRAWER		Bai, Brain	2008-AGU-25	POWER		DATE		INVENTEC			
DESIGN		Bai, Brain	2008-AGU-25								
CHECK		Bai, Brain	2008-AGU-25								
RESPONSIBLE											
SIZE =		3		VER :		A04		TITLE		MIMAS	
FILE NAME :		XXXX-XXXXXX-XX						SIZE	CODE	DOC NUMBER	REV
P/N :		6050A2215101						A3	CS	6050A2350601	X02
								SHEET	1	OF	31

2008/10/01	2008-ECO-016553	B
DATE	CHANGE NO.	REV

TABLE OF CONTENTS

PAGE

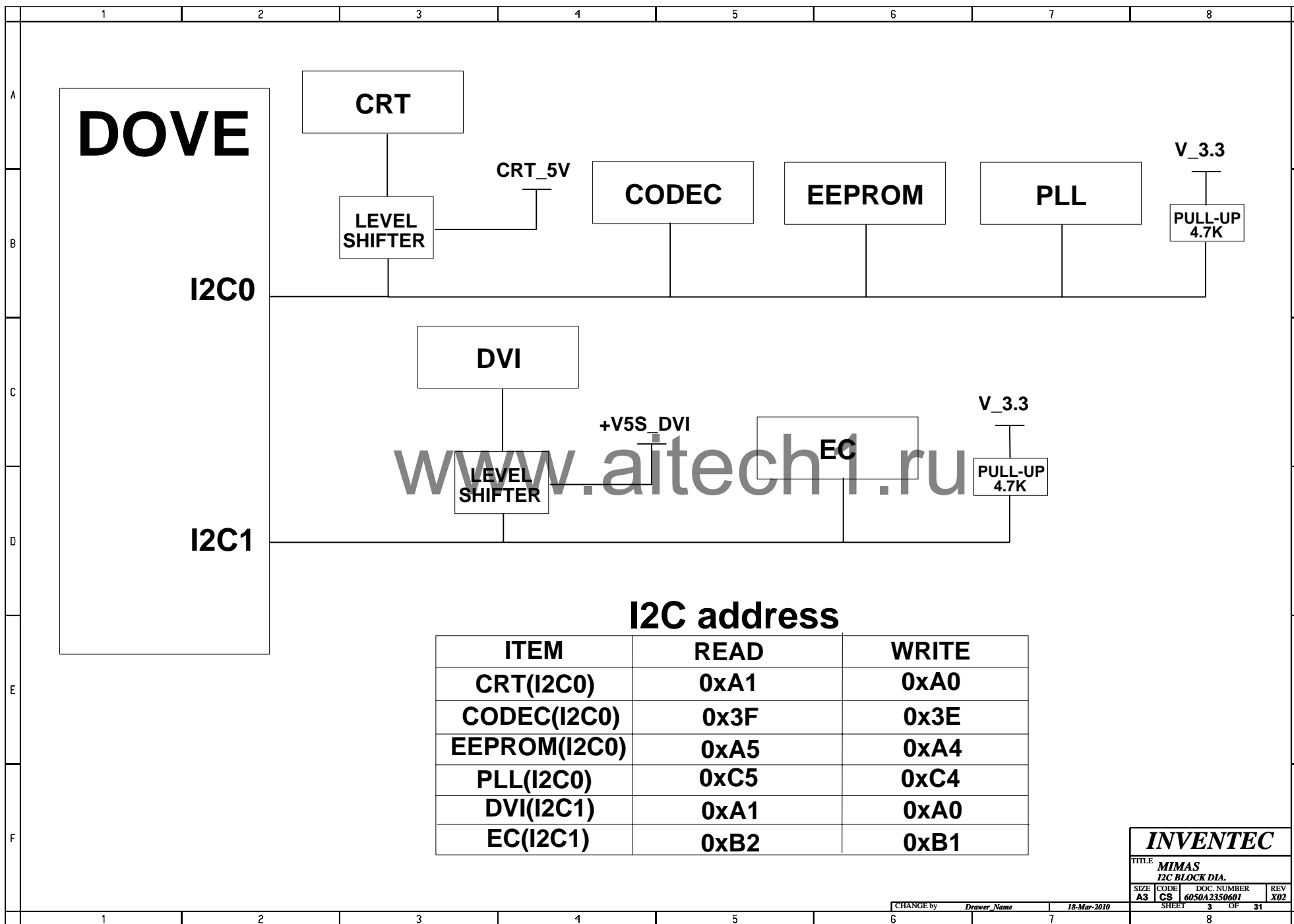
1. COVER PAGE
2. INDEX
3. I2C BLOCK DIAGRAM
4. BLOCK DIAGRAM(DESKTOP)
5. POWER SEQUENCE BLOCK
6. DC JACK & VOLTAGE RESET
7. 3.3V & 5.0V
8. 1.8V & 2.5V
9. 1.5V & VTT
10. VCORE & VCPU
11. POWER LOW SWITCH
12. DOVE1
13. DOVE2
14. DOVE3
15. DDR TERMINATOR
16. DDR MEMORY
17. DDR MEMORY
18. DVI TRANSMITTER
19. DVI CONN
20. SD & SATA CONN
21. CONFUGRATION (STRAPPING)
22. GIGA PHY & RJ45
23. USB HUB & 9-PIN HEADER
24. USB PORT & USB CONN
25. MINI CARD & KB CONN.
26. AUDIO JACK
27. STATUS LED
28. POWER UP & RESET
29. MCU & POWER BUTTON
30. D-sub

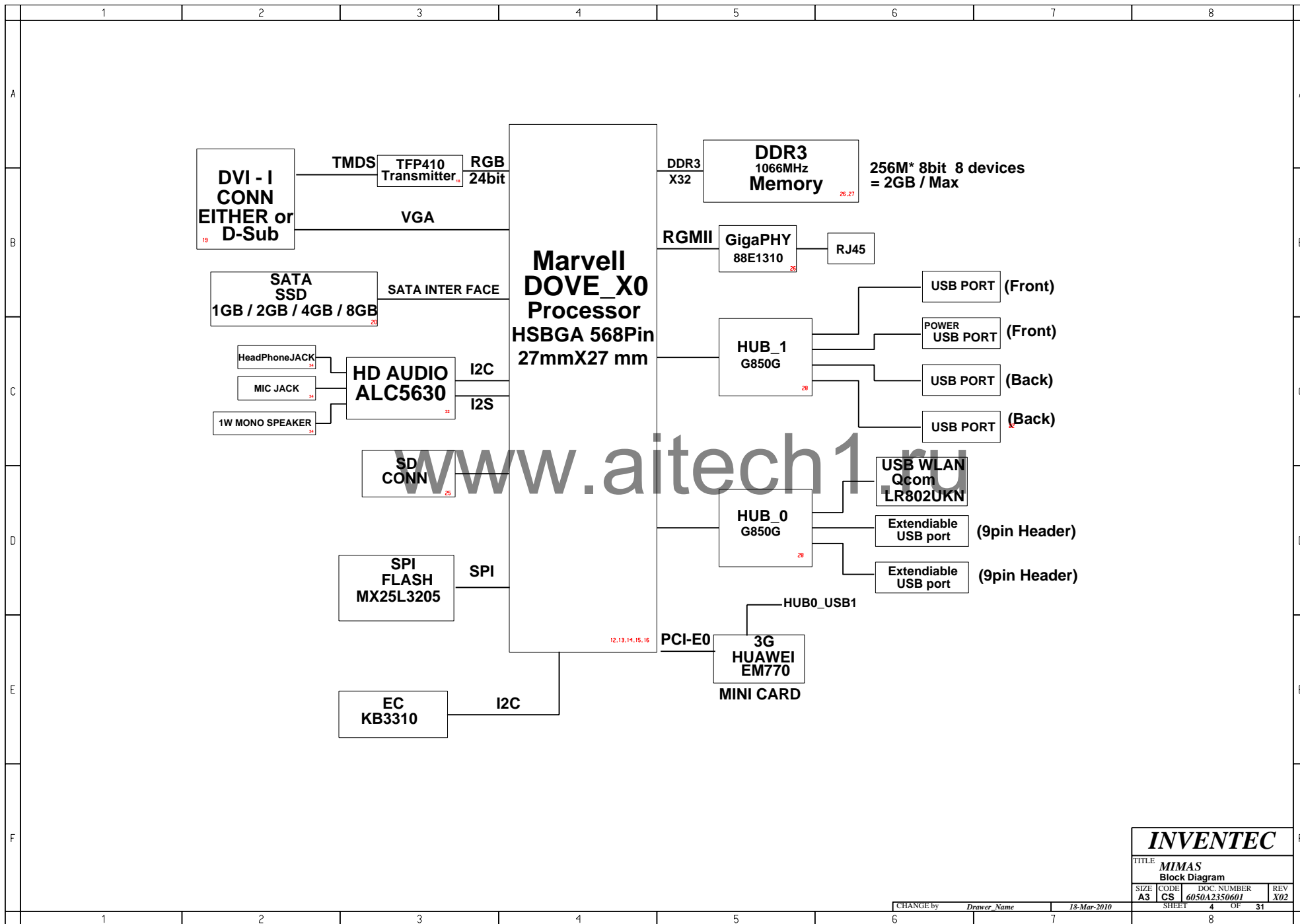
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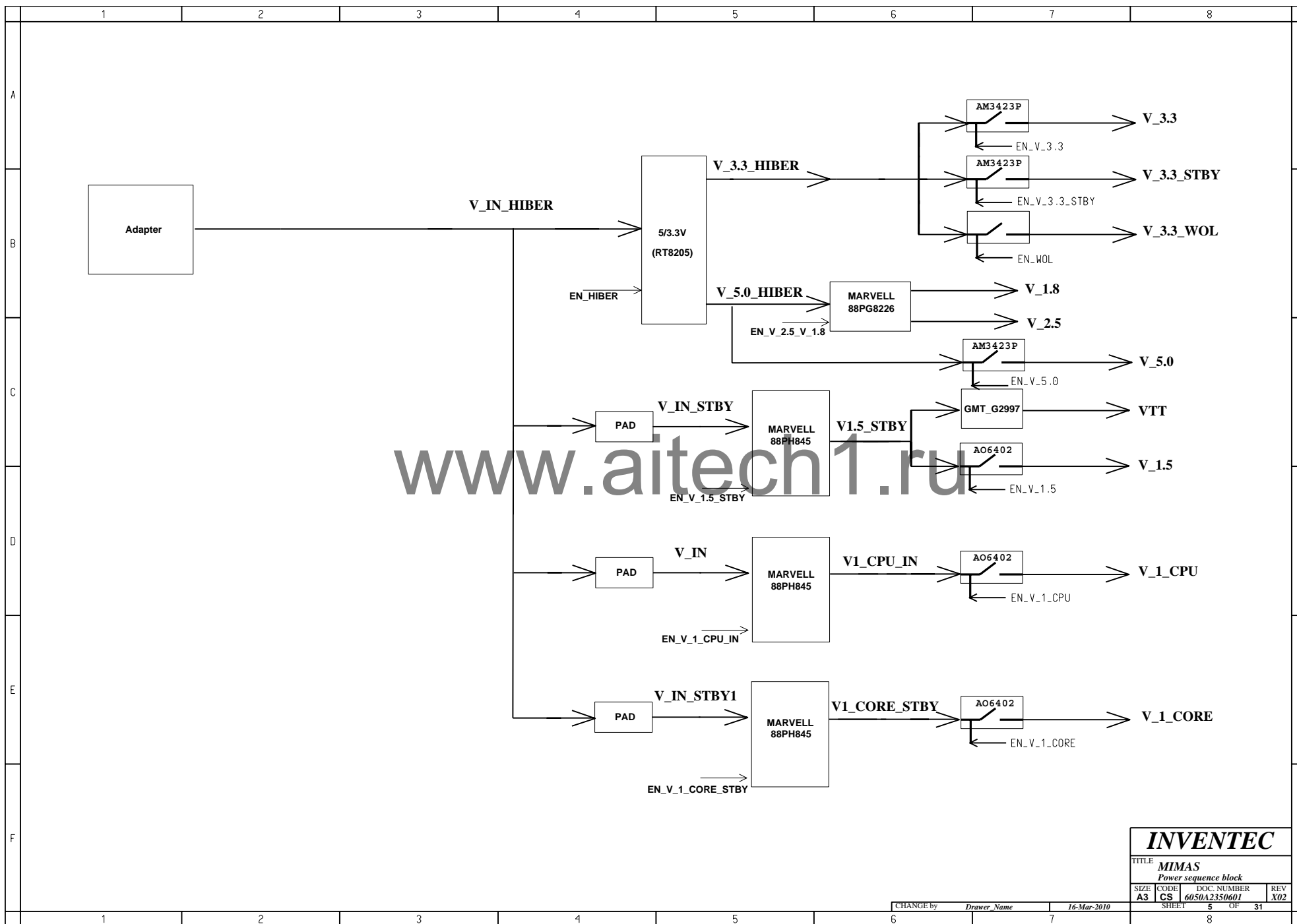
TITLE **MIMAS**

SIZE A3	CODE CS	DOC. NUMBER 6050A2350601	REV X02
SHEET 2		OF 31	

CHANGE by *Drawer Name* 16-Mar-2010







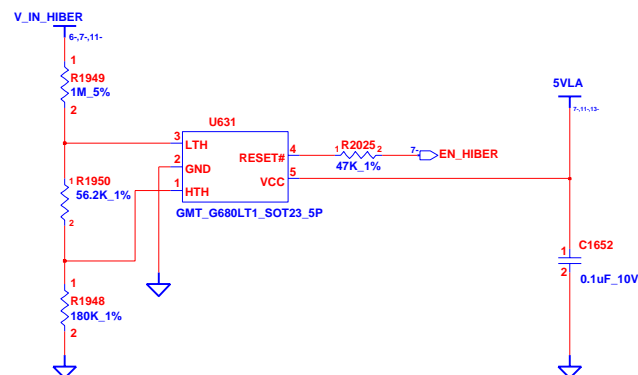
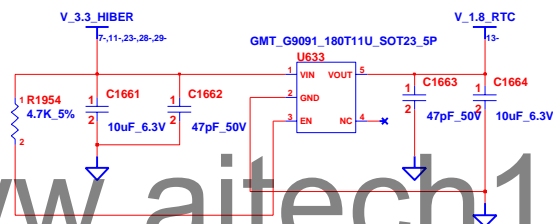
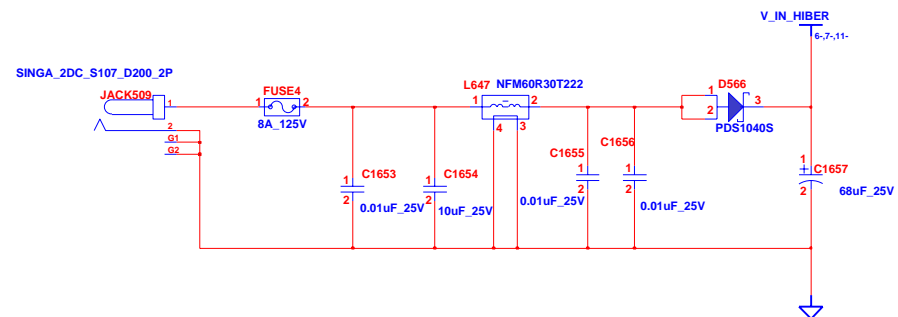
INVENTEC

TITLE: **MIMAS**
Power sequence block

SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A2350601	X02

CHANGE by: *Drawer Name* 16-Mar-2010

SHEET 5 OF 31

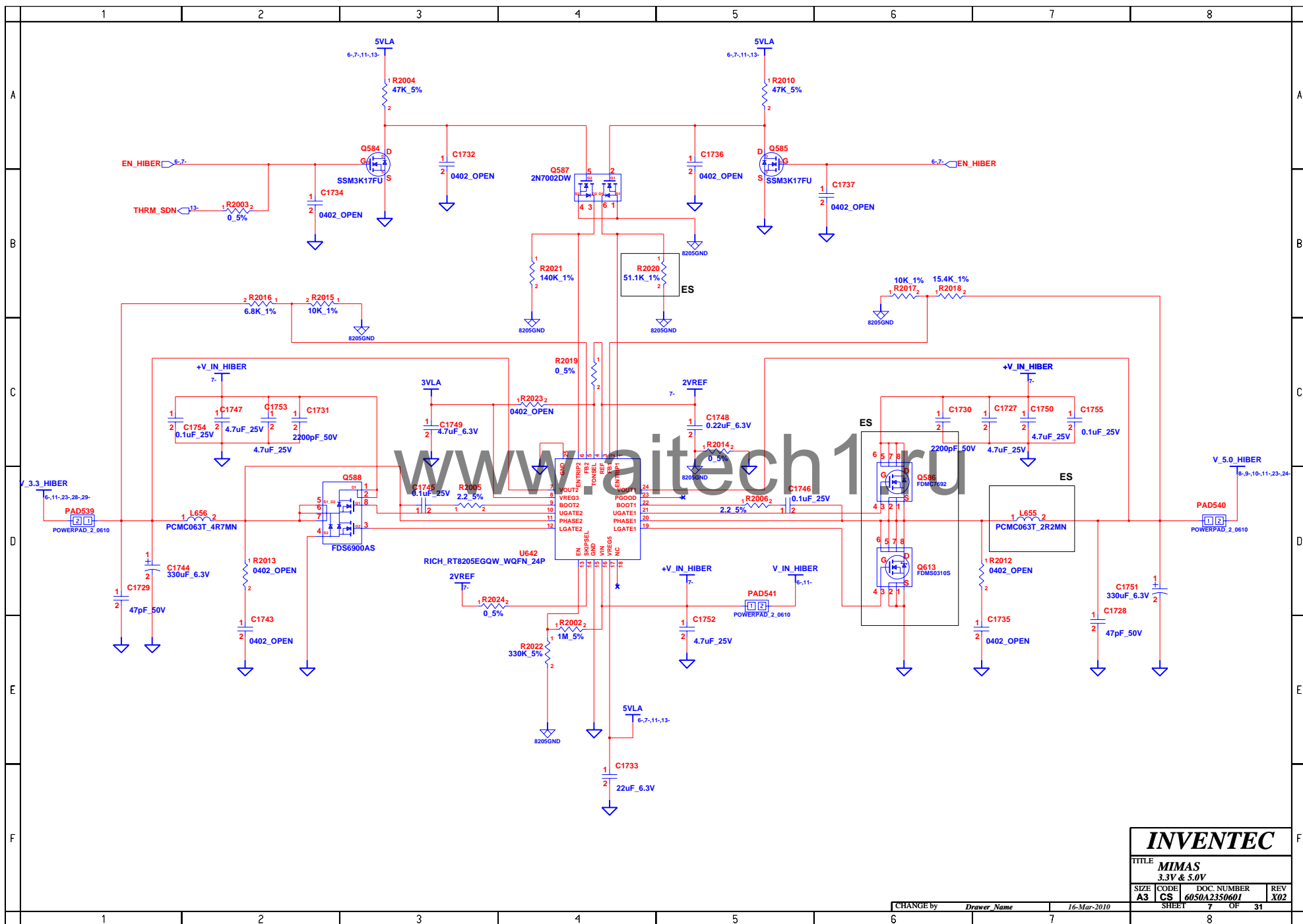


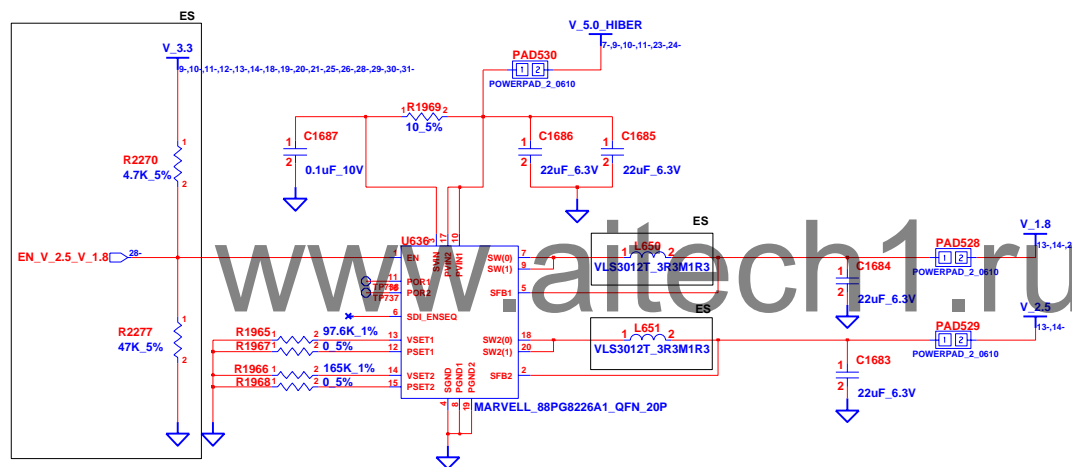
INVENTEC

TITLE			
MIMAS			
DC JACK & VOLTAGE RESET			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A2350601	X02

CHANGE by *Drawer Name* 16-Mar-2010

SHEET 6 OF 31





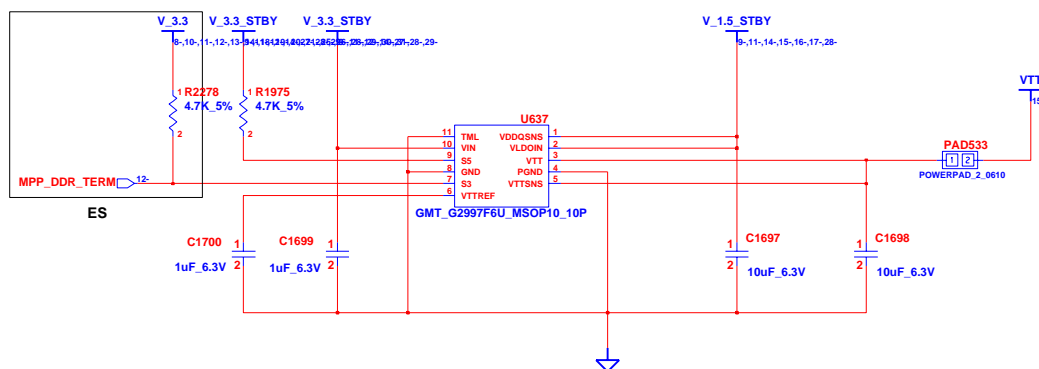
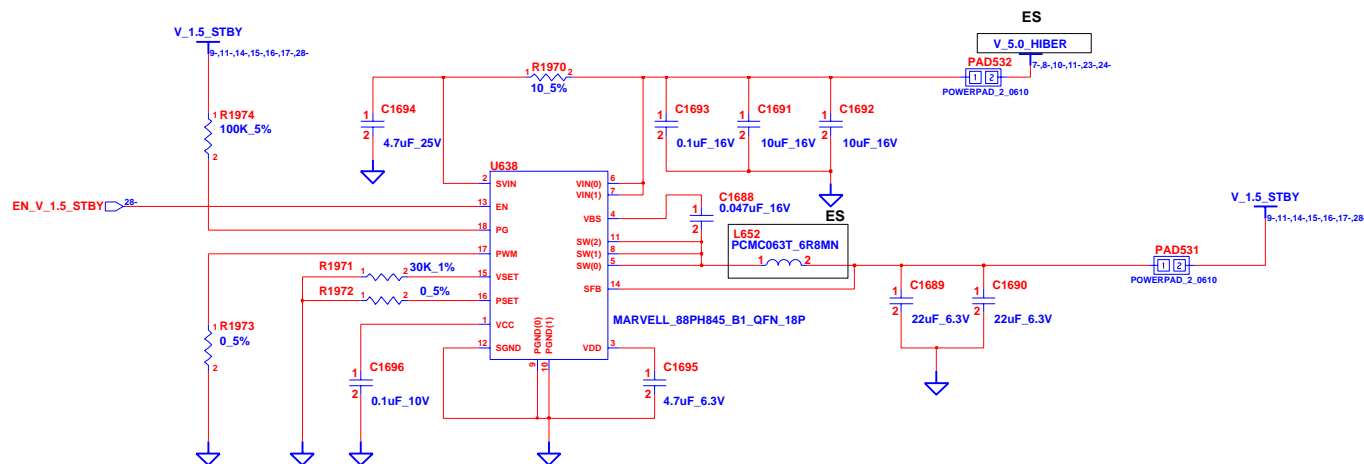
INVENTEC

TITLE
MIMAS
1.8V & 2.5V

SIZE A3	CODE CS	DOC. NUMBER 6050A2350601	REV X02
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CHANGE by *Drawer Name* 16-Mar-2010

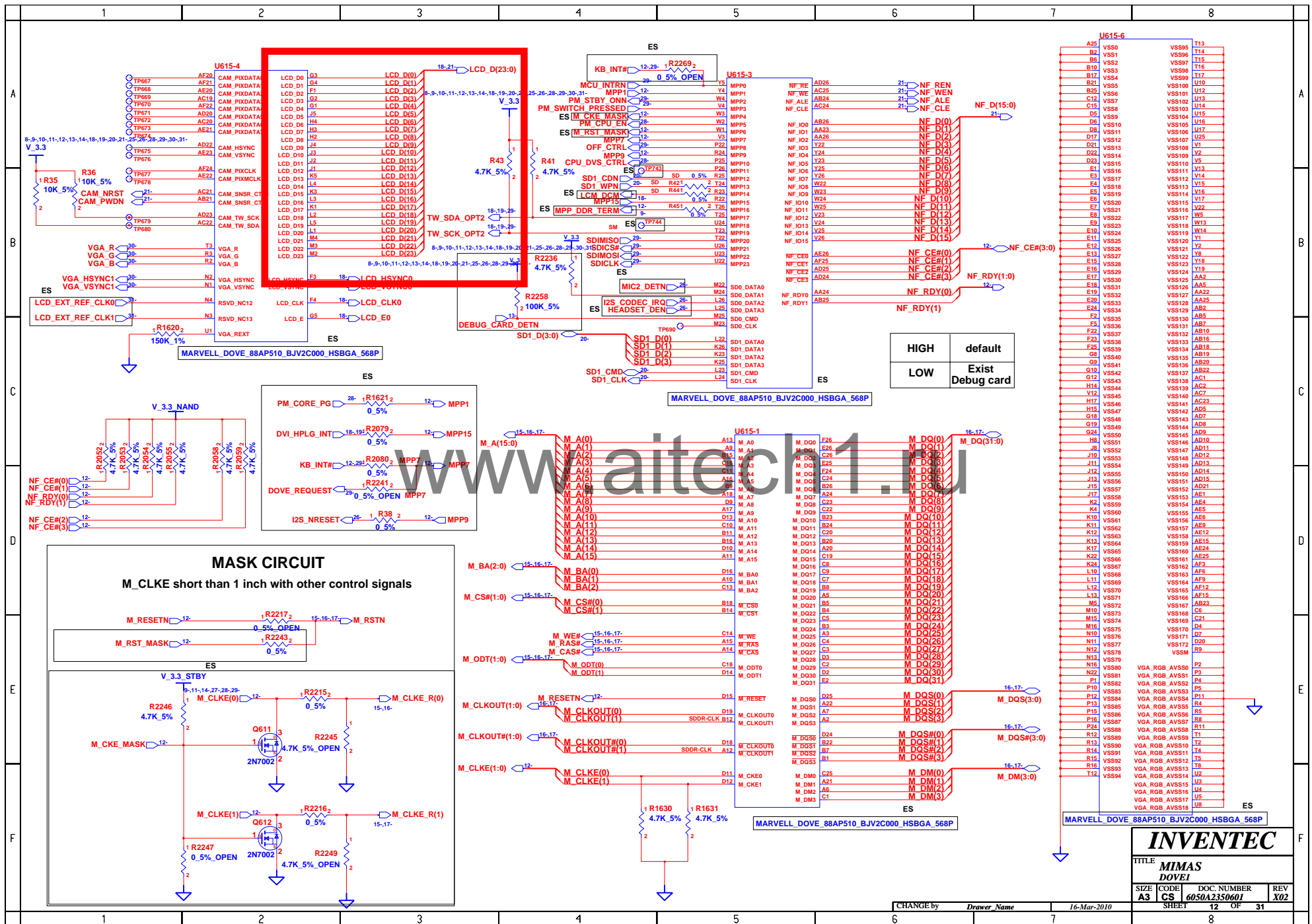
SHEET 8 OF 31

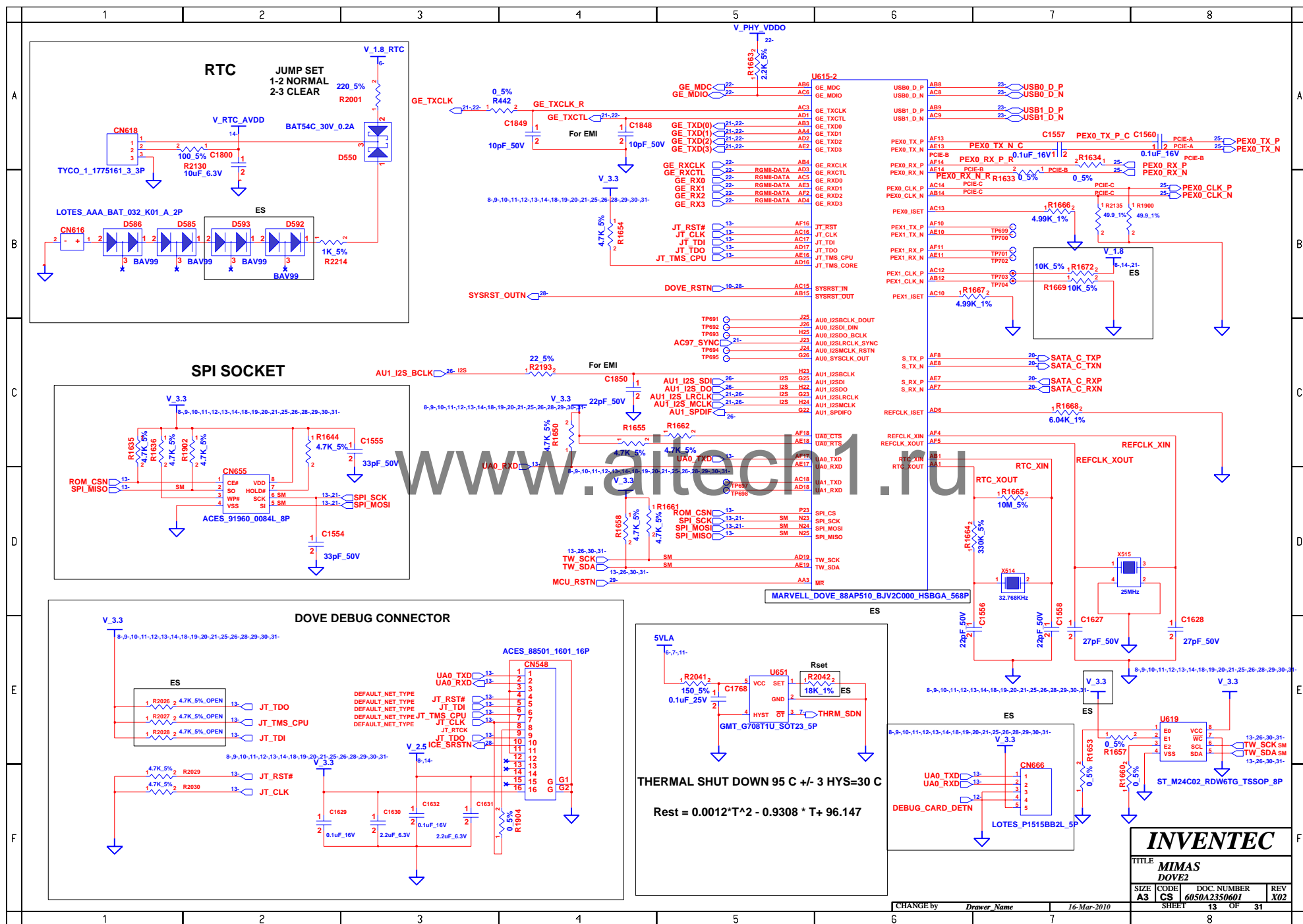


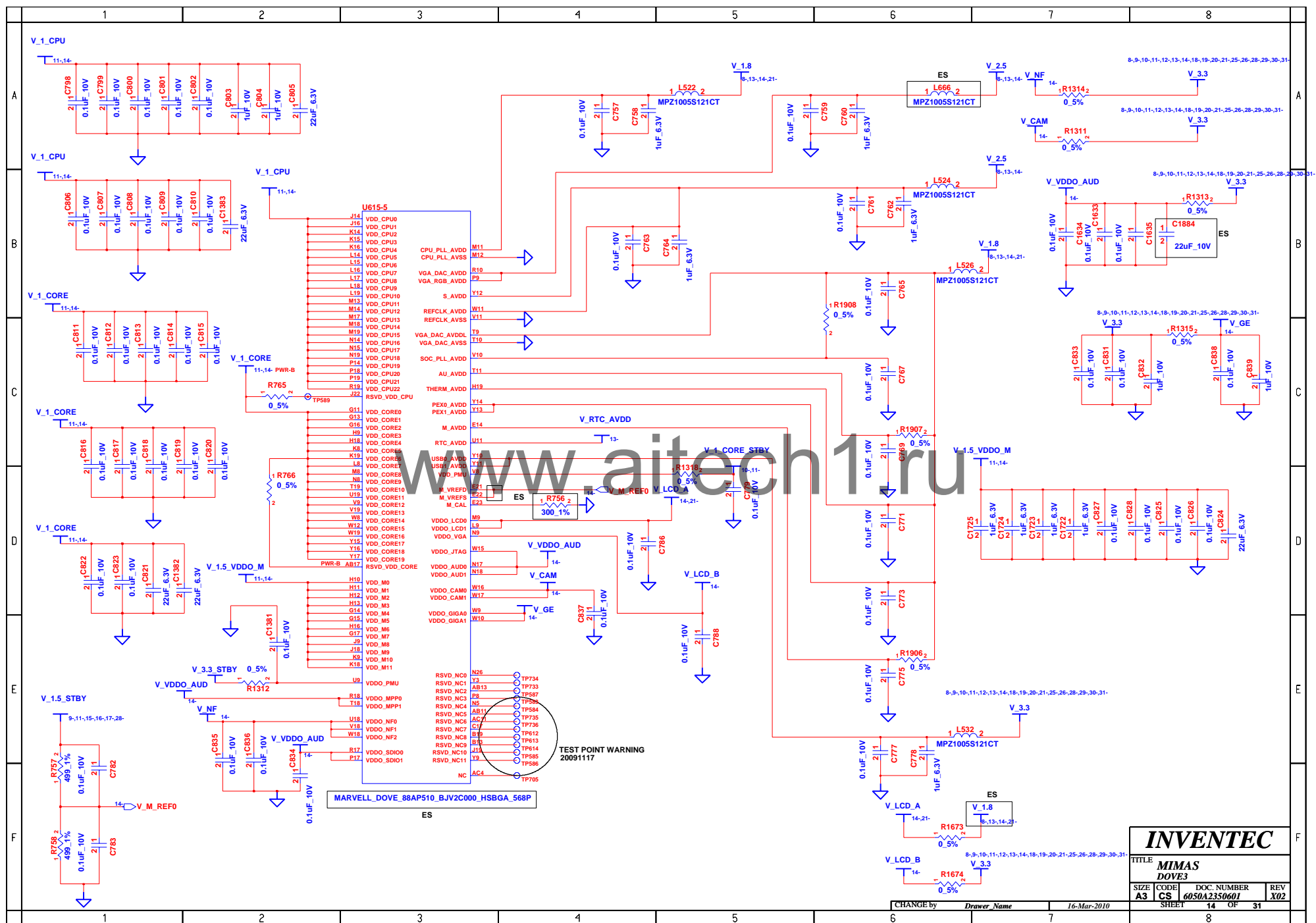
www.aitech1.ru

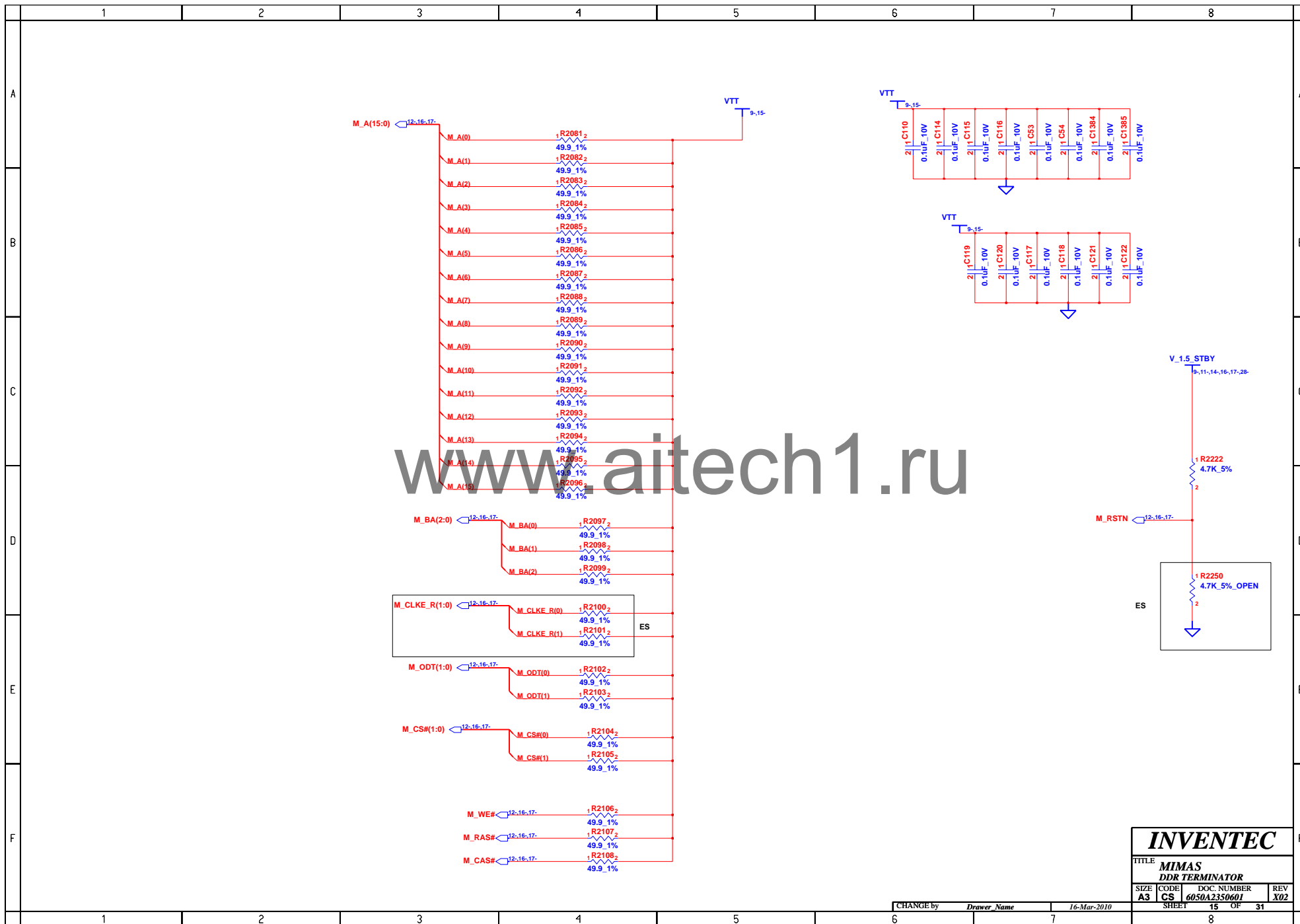
INVENTEC			
TITLE			
MIMAS			
SYSTEM POWER (3V7 / 1V8_DIG1)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A2350601	X02
SHEET		9	OF 31

CHANGE by *Drawer Name* 16-Mar-2010





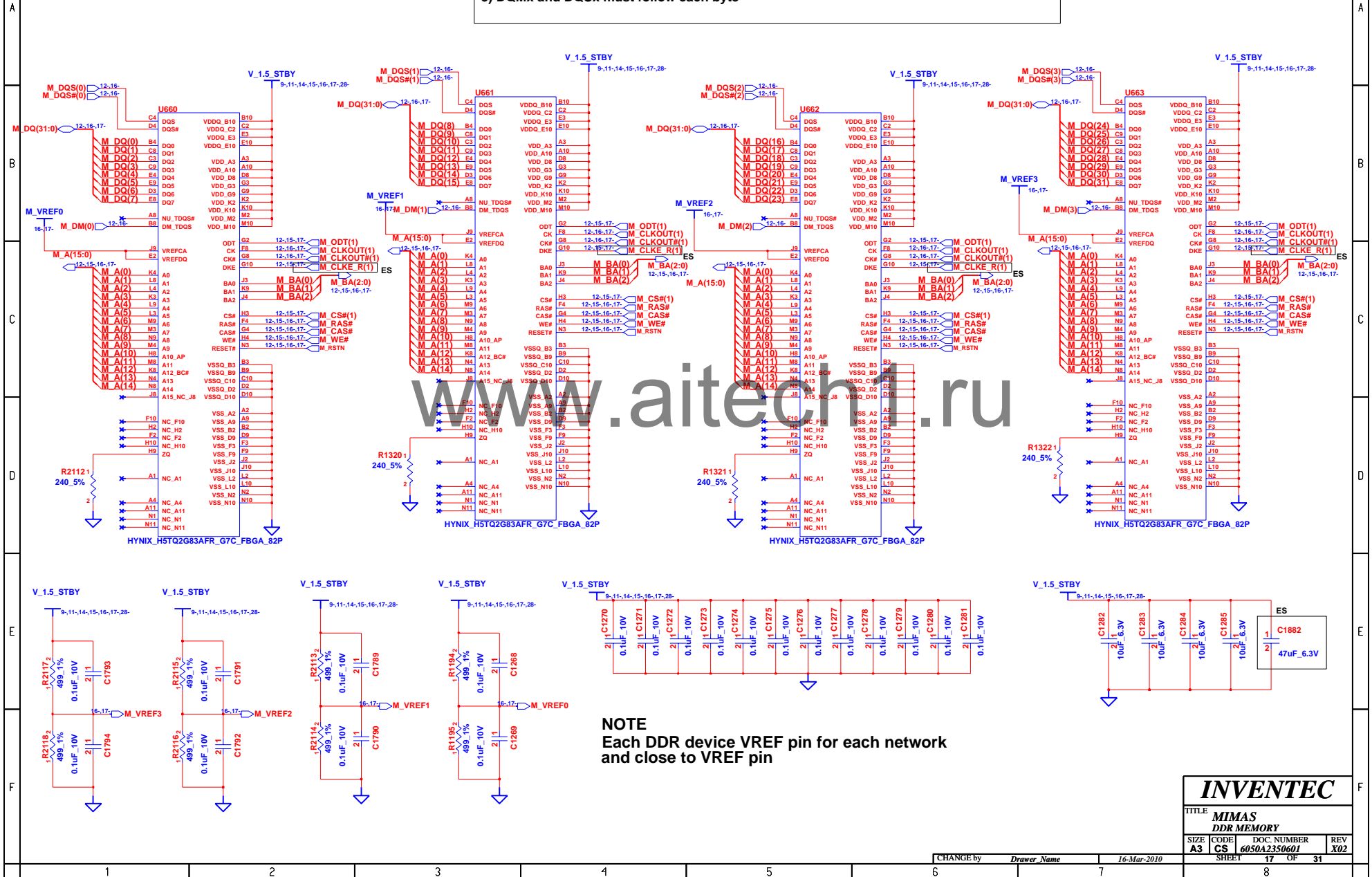




INVENTEC			
TITLE			
MIMAS			
DDR TERMINATOR			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A2350601	X02
SHEET		15	OF 31

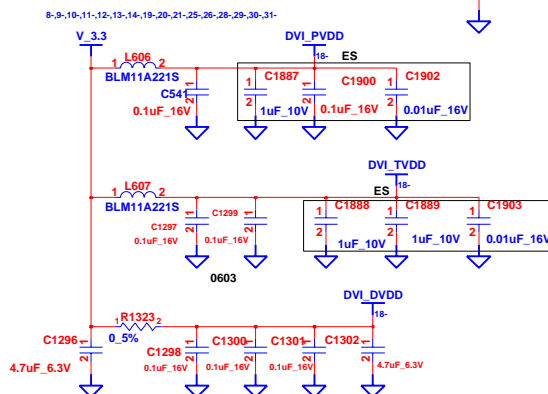
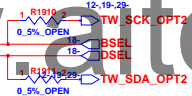
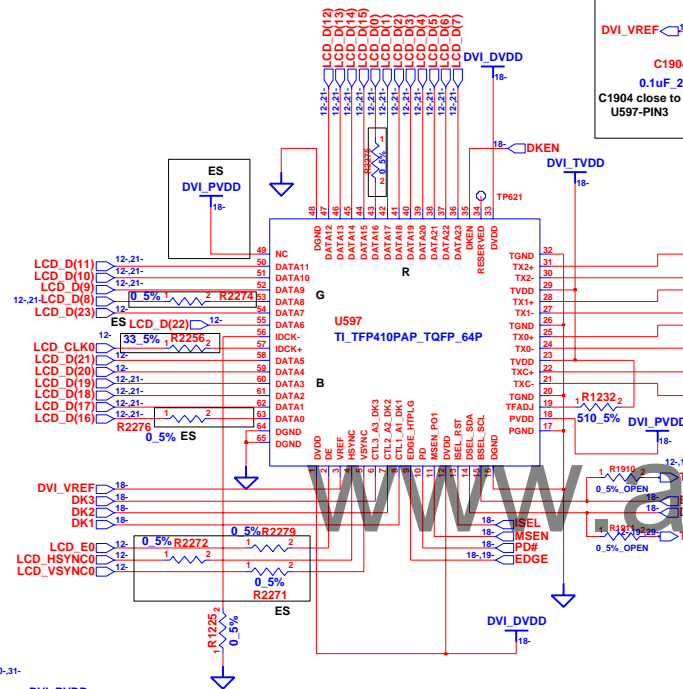
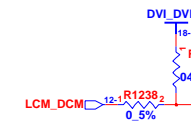
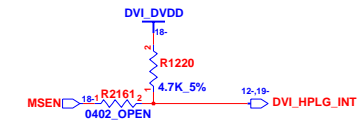
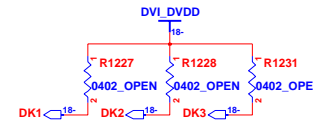
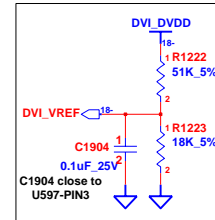
CHANGE by *Drawer Name* 16-Mar-2010

NOTE: DDR Data pins can be swapped for improved routin according to the following rules:
 1) Data pins can be swapped within each byte
 2) Data bytes can be swapped
 3) DQMx and DQSx must follow each byte



Disable SMBus use strapping setting

two signal need conn. to CPU GPIO
One is MSEM
The other is EDGE/HTPLG



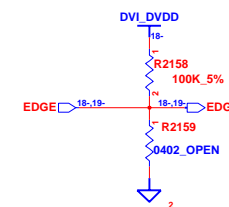
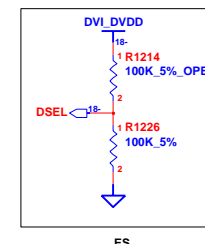
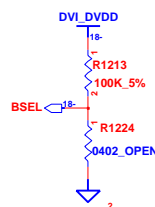
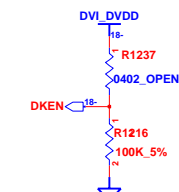
R1221	R430	Mode
NC	1	I2C disable <default>
1	NC	I2C enable

R1213	R1224	Mode
1	NC	24 bit Input/Single-edge

R1214	R1226	Mode
NC	1	24 bit Single-ended

R2158	R2159	Mode
1	NC	rising edge
NC	1	falling edge

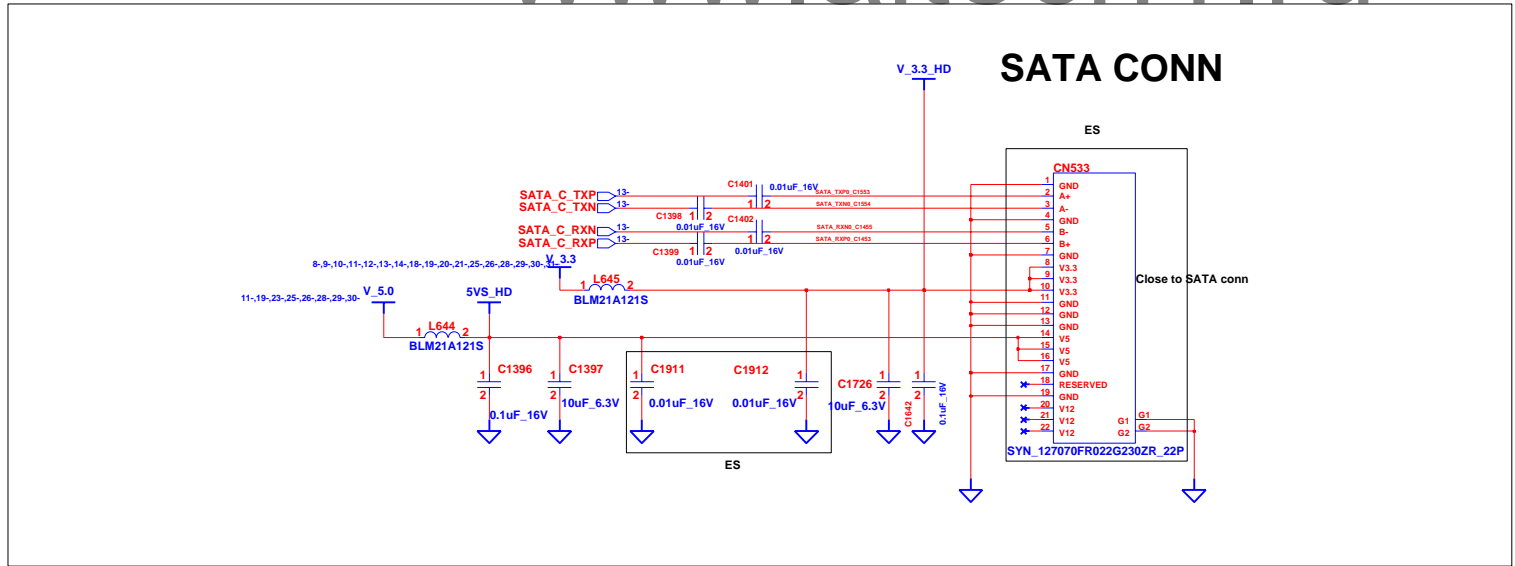
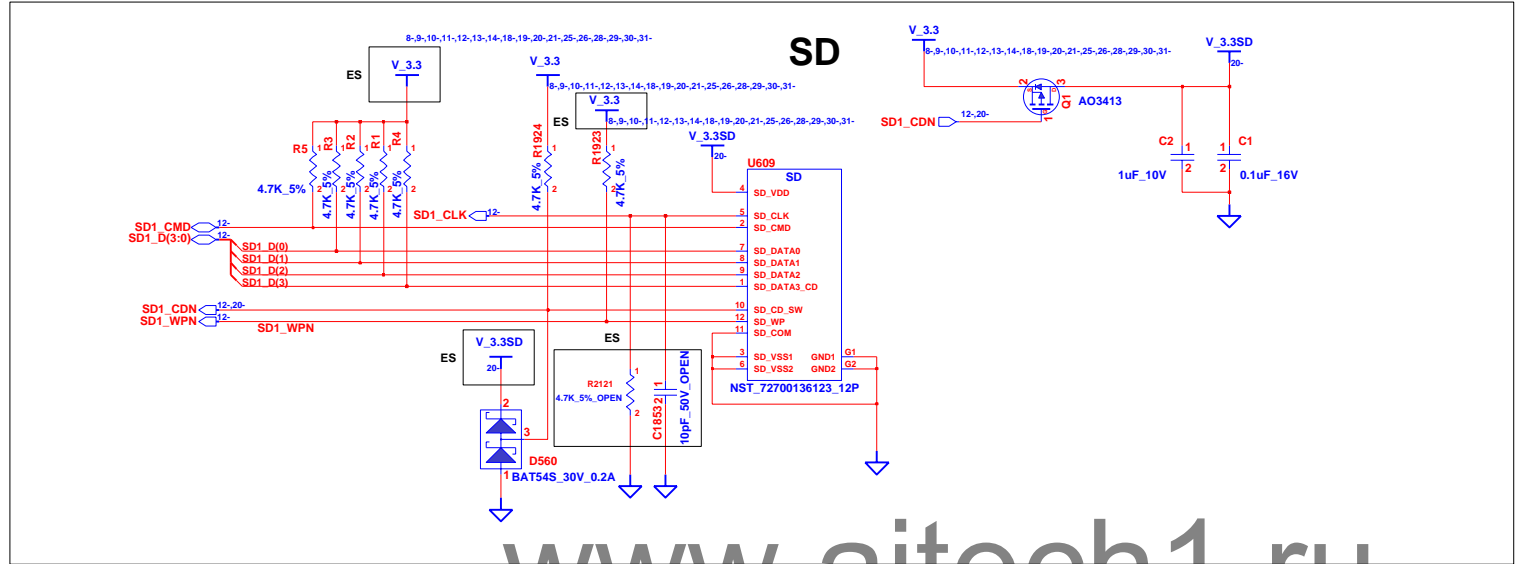
R1237	R1216	Mode
NC	1	de-skew <default>
1	NC	de-skew setting by DK1~DK3



DVI Transmitter

<i>INVENTEC</i>			
TITLE <i>MIMAS</i> <i>DVI TRANSMITTER</i>			
SIZE A3	CODE CS	DOC. NUMBER 6050A2350601	REV X02
SHEET		18	OF 31

INVENTEC			
TITLE MIMAS DVI CONN			
SIZE A3	CODE CS	DOC. NUMBER 6050A2350601	REV X02
SHEET		19	OF 31

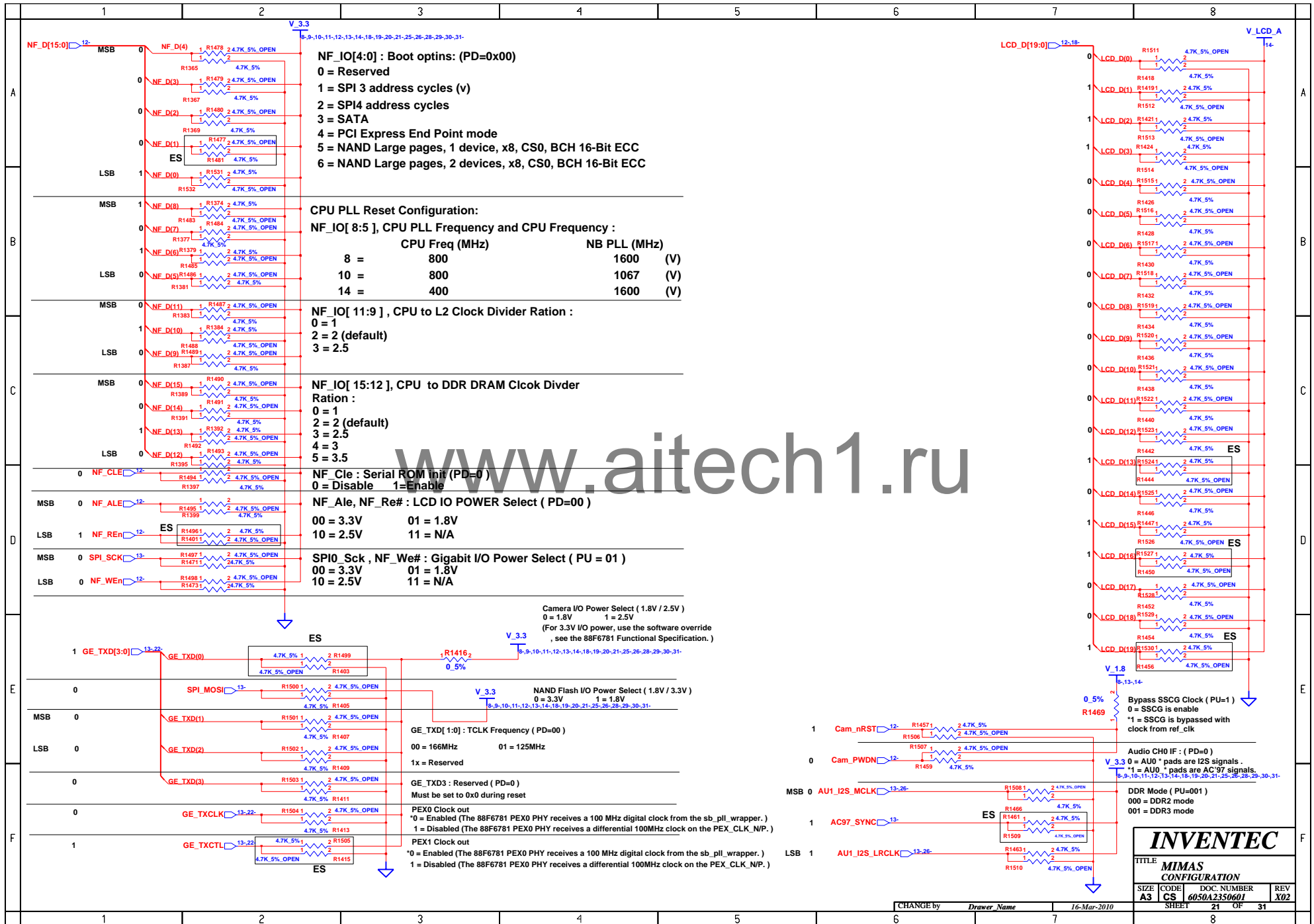


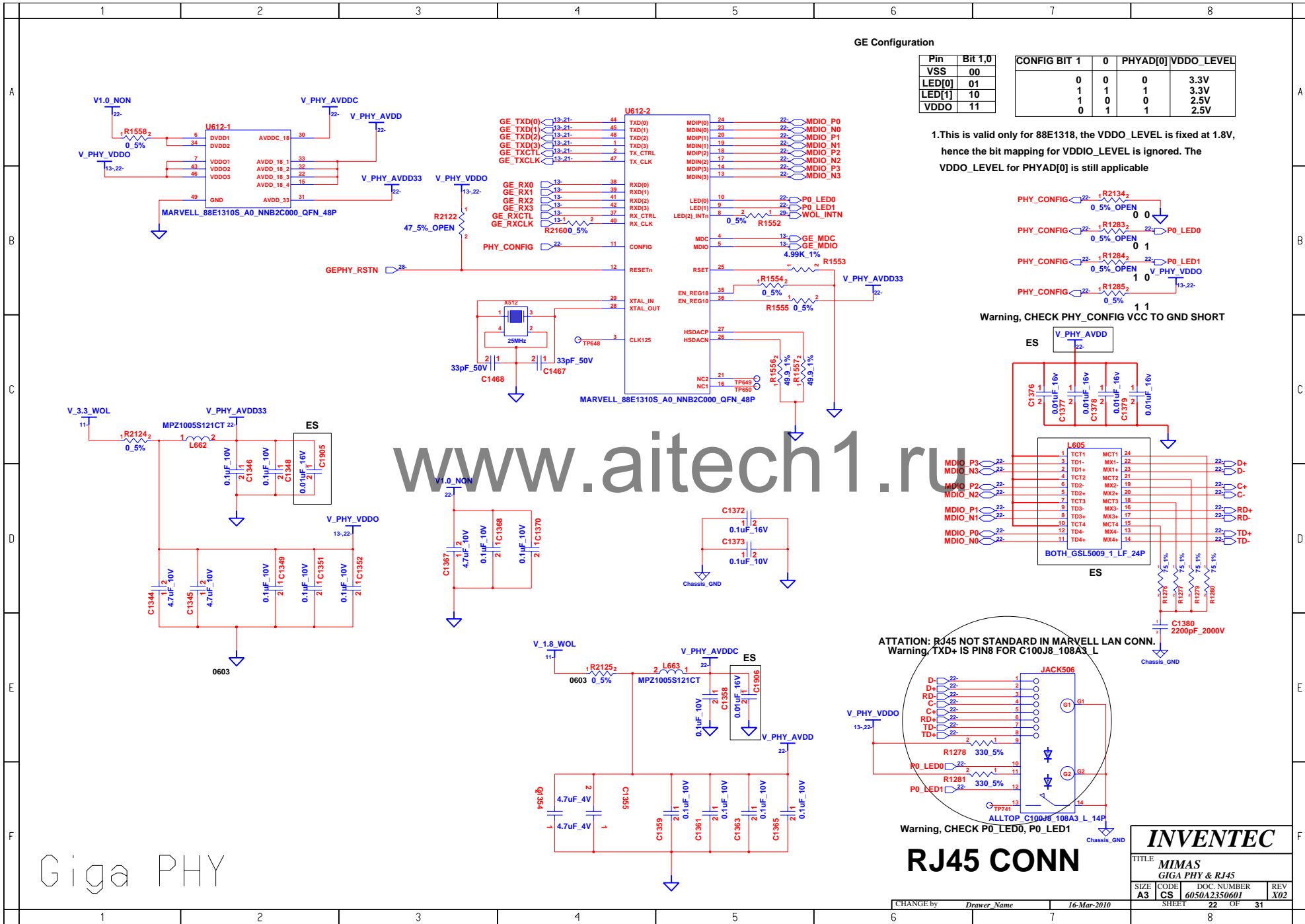
SD & SATA

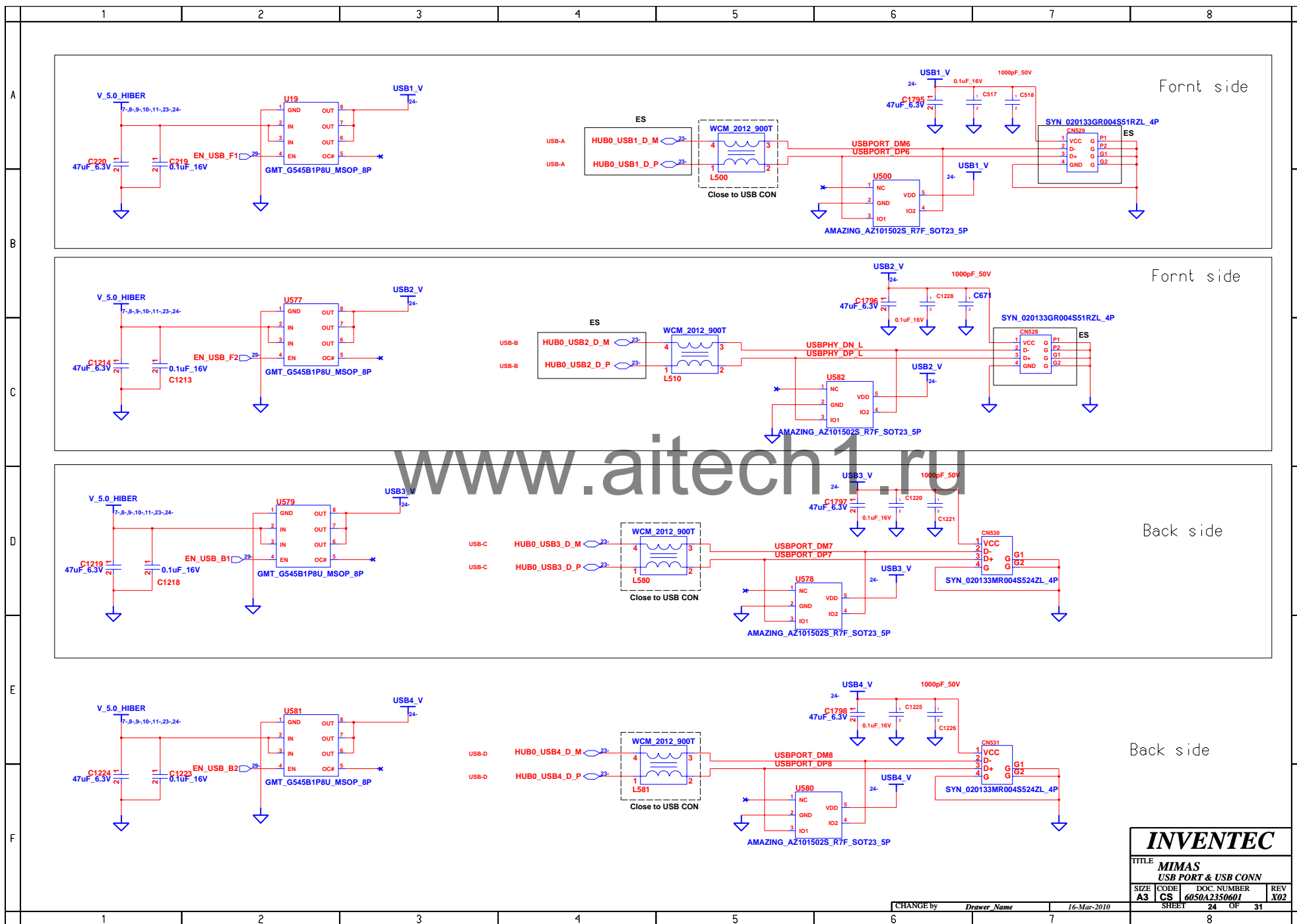
INVENTEC

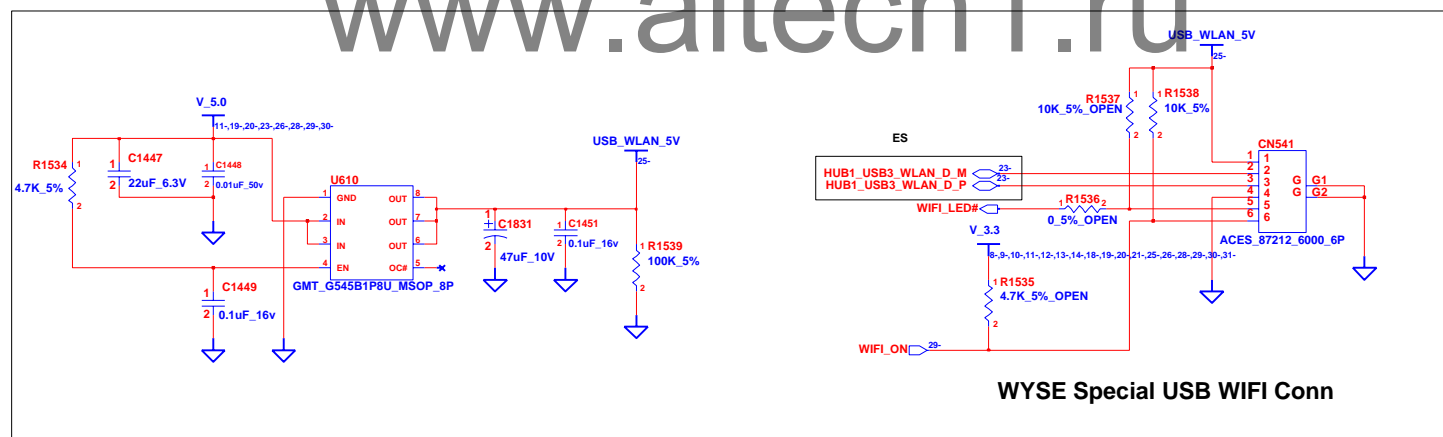
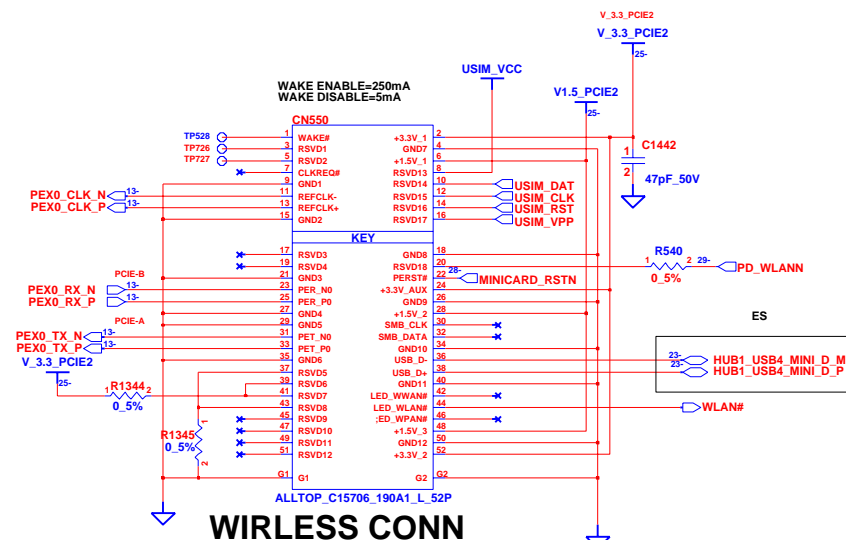
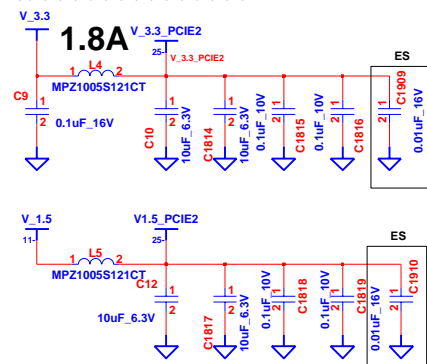
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MIMAS			
SD& SATA CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	6050A2350601	X02
SHEET		20	OF 31

CHANGE by *Drawer Name* 16-Mar-2010









3G & WIFI & WLAN CONN

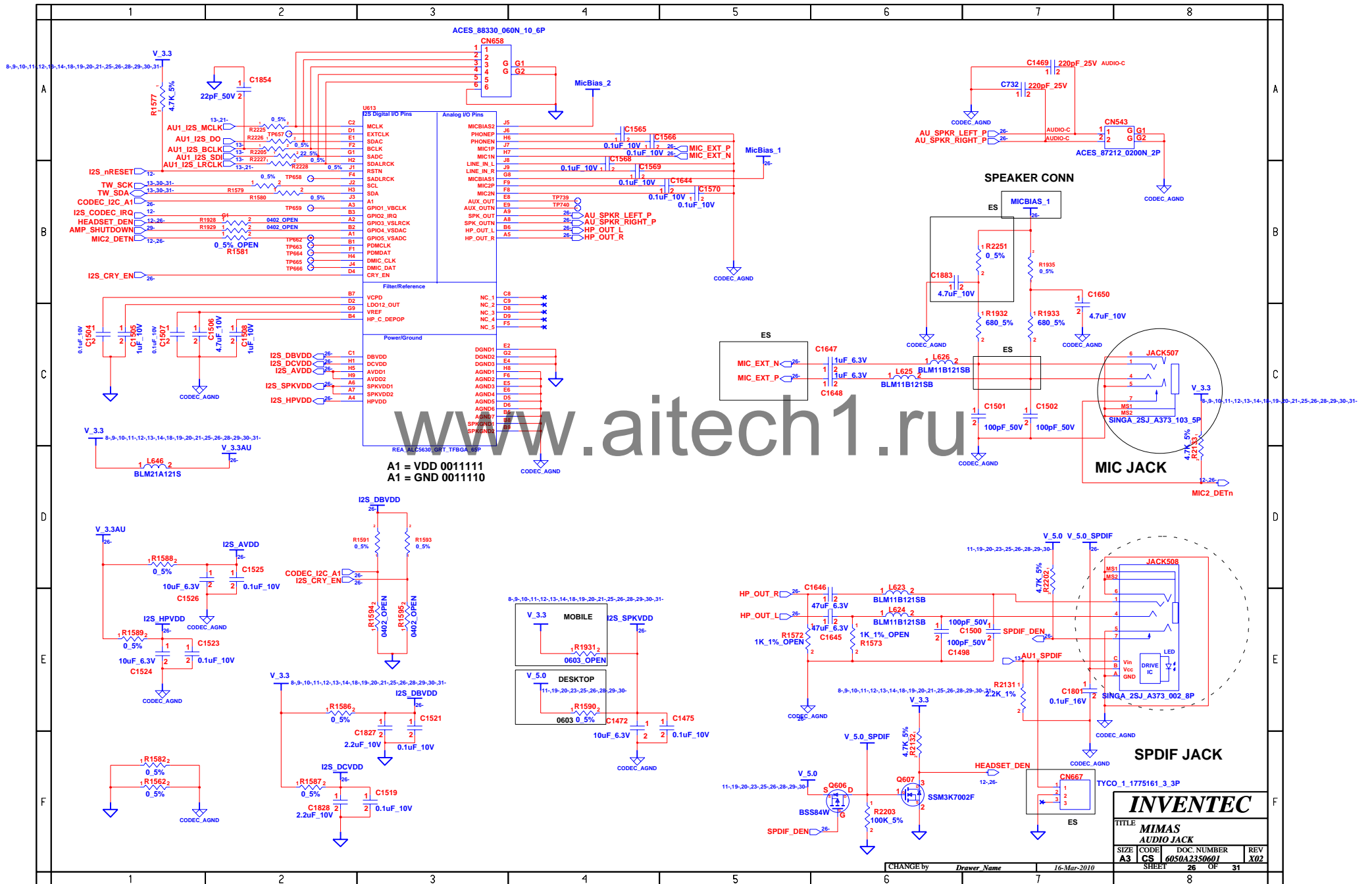
INVENTEC

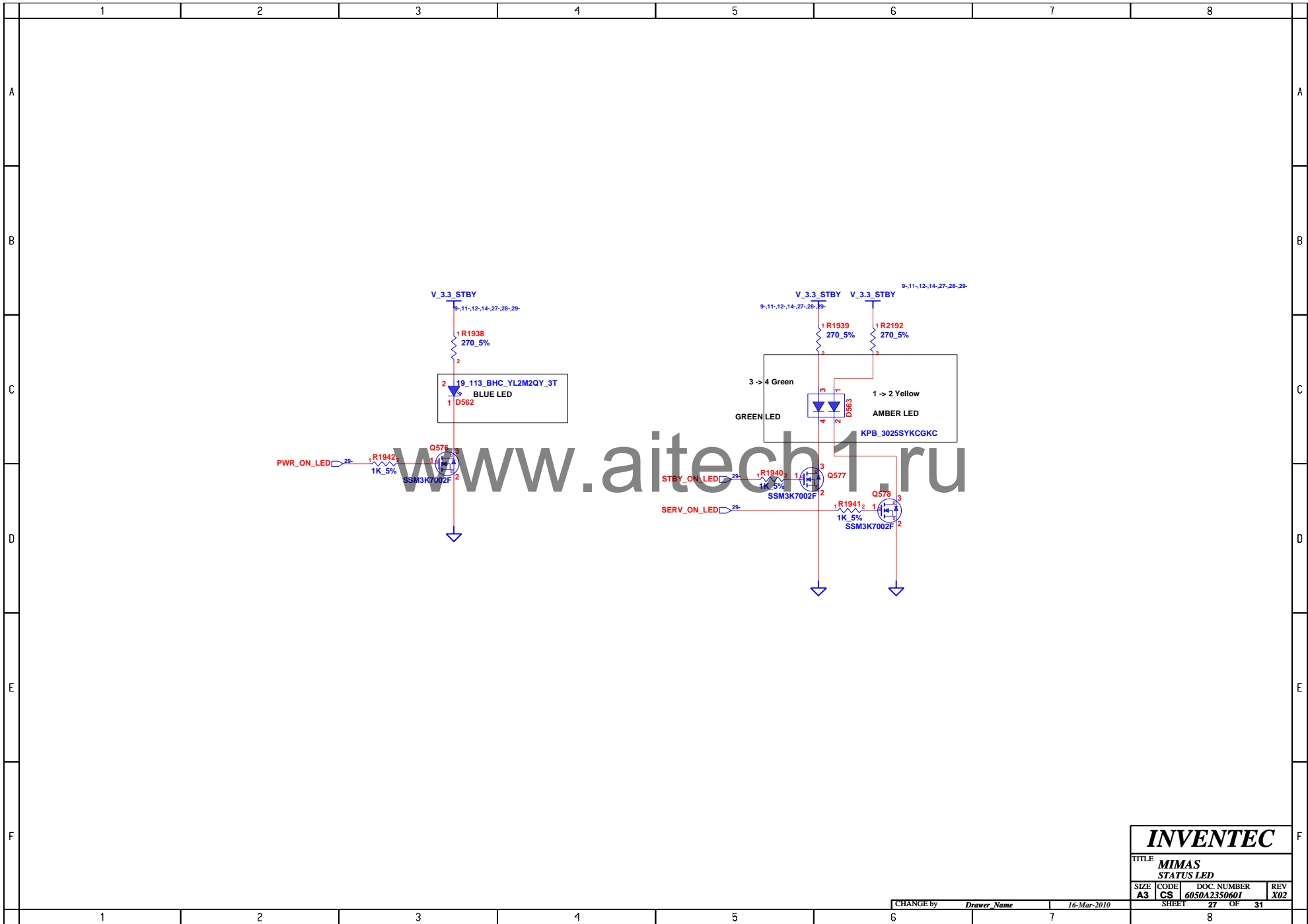
TITLE	MIMAS MINI CARD & KB CONN
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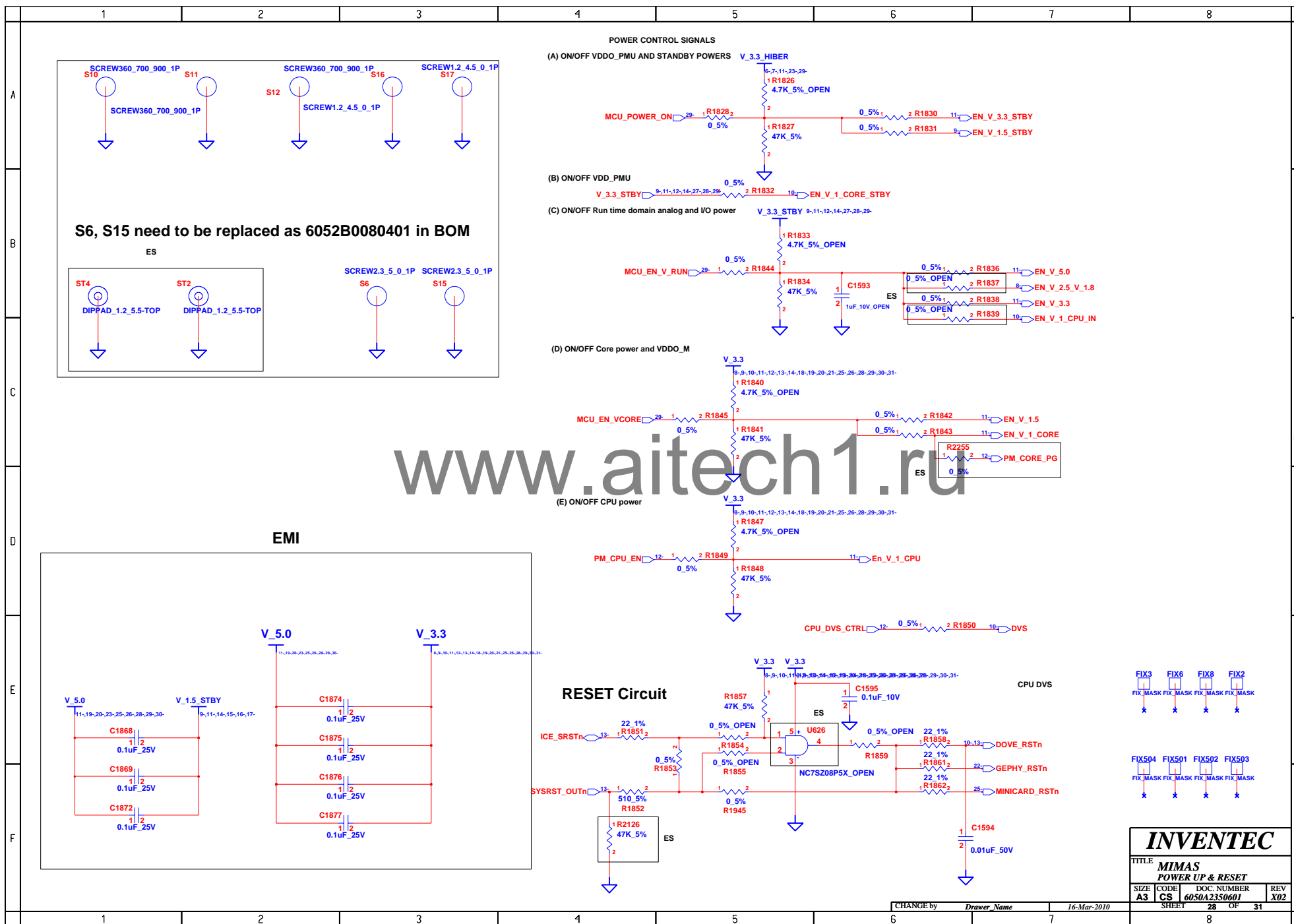
SIZE A3	CODE CS	DOC. NUMBER 6050A2350601	REV X02
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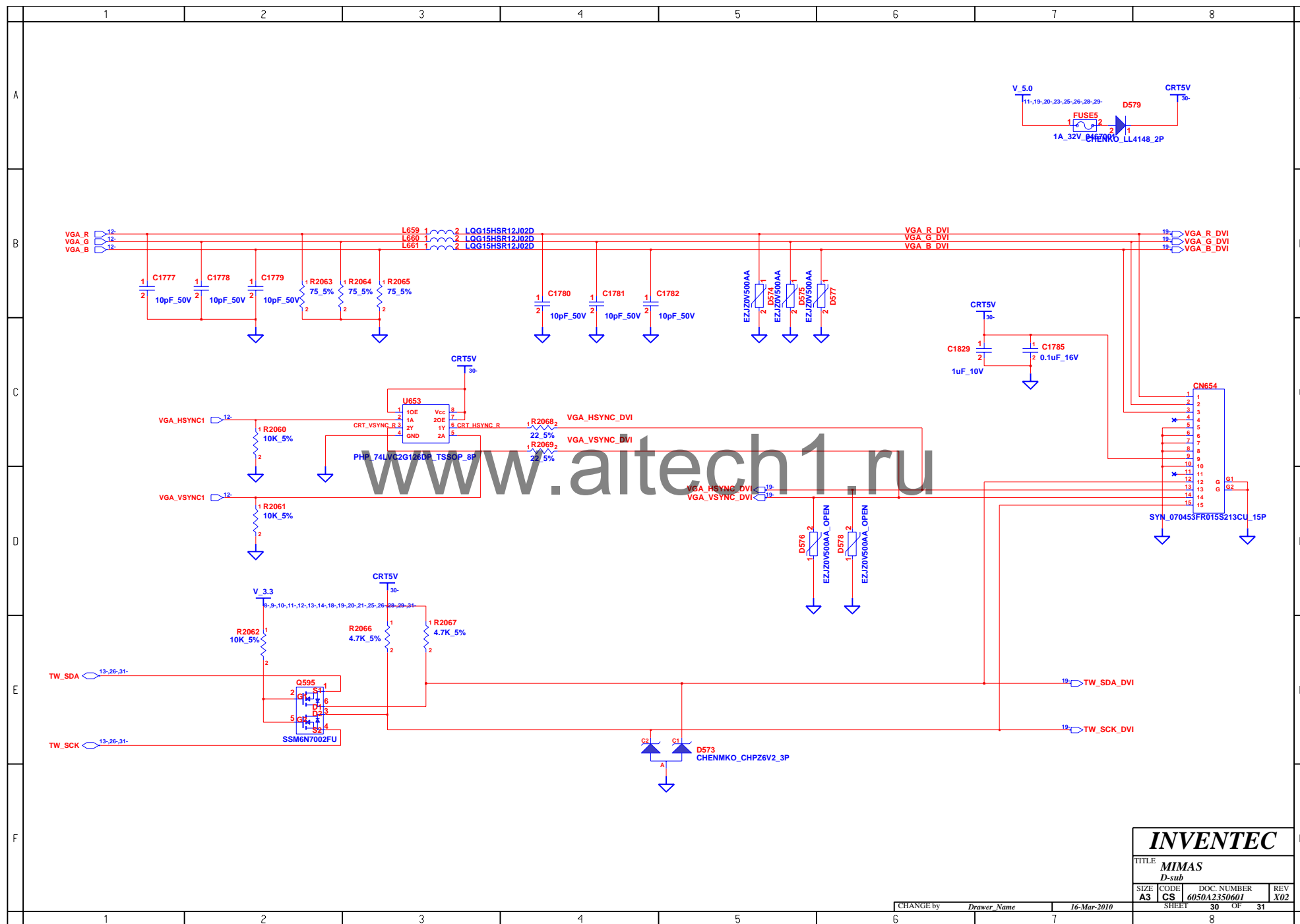
CHANGE by	<i>Drawer Name</i>	16-Mar-2010
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SHEET 25 OF 31









INVENTEC				
TITLE				
MIMAS				
D-sub				
SIZE	CODE	DOC. NUMBER	REV	
A3	CS	6050A2350601	X02	
CHANGE by		Drawer Name	16-Mar-2010	
SHEET		30	OF	31
		8		



CHANGE by	Drawer_Name	16-Mar-2010
6	7	